

*** NOTICES ***

JPO and NCIPi are not responsible for any damages caused by the use of this translation.

1. This document has been translated by computer. So the translation may not reflect the original precisely.
2. **** shows the word which can not be translated.
3. In the drawings, any words are not translated.

DESCRIPTION OF DRAWINGS

[Brief Description of the Drawings]

[Drawing 1] It is the schematic diagram showing an example of this invention.

[Drawing 2] It is a wave form chart for explaining this invention.

[Drawing 3] It is the schematic diagram showing other examples of this invention.

[Drawing 4] It is the schematic diagram showing other examples of this invention.

[Drawing 5] It is drawing for explaining a TDMA system.

[Drawing 6] It is a schematic diagram for explaining this invention.

[Description of Notations]

10 Sending Circuit

13 Quadrature Modulation Circuit

14 Gain Control Circuit

15 Power Amplification

20 System Controller

30 APC Circuit

33 Electrical-Potential-Difference Comparator Circuit

41 Envelope Detector

42 Subtractor Circuit

43 Sampling Hold Circuit

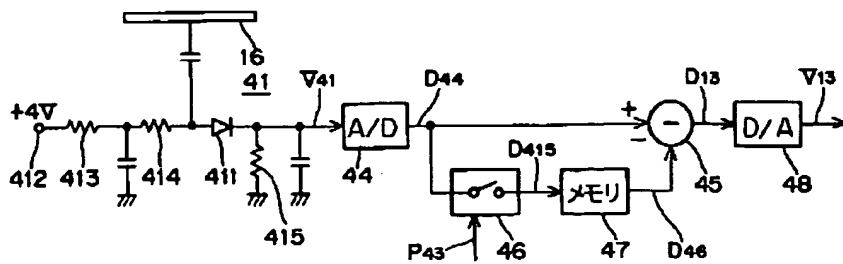
[Translation done.]

JPO and NCIPI are not responsible for any damages caused by the use of this translation.

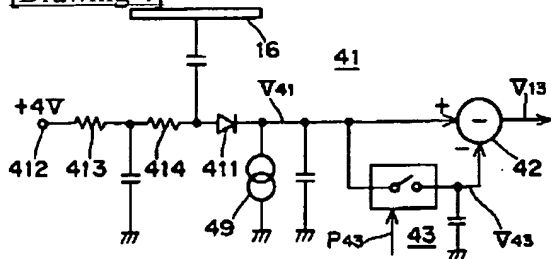
- ## DRAWINGS

Figure 1 is a block diagram of a transmission system. The system includes a transmission path (送信回路) and a receiver path (受信回路). The transmission path consists of blocks 11 (送), 12 (処理), 13 (変調), 14 (利得), 15 (増幅), and 17 (出力). The receiver path includes block 18 (共) and 19 (アンテナ). A control path (30 APC回路) includes blocks 20 (システム), 31 (ラッチ), and 32 (D/A). The control path also includes a voltage divider network (41, 42, 43) and a switch (44).

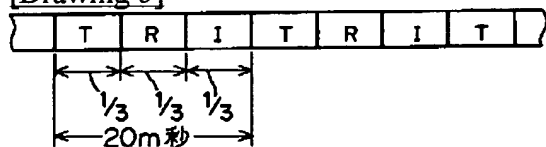
http://www4.ipdl.ncipi.go.jp/cgi-bin/tran_web/cgi_ejje



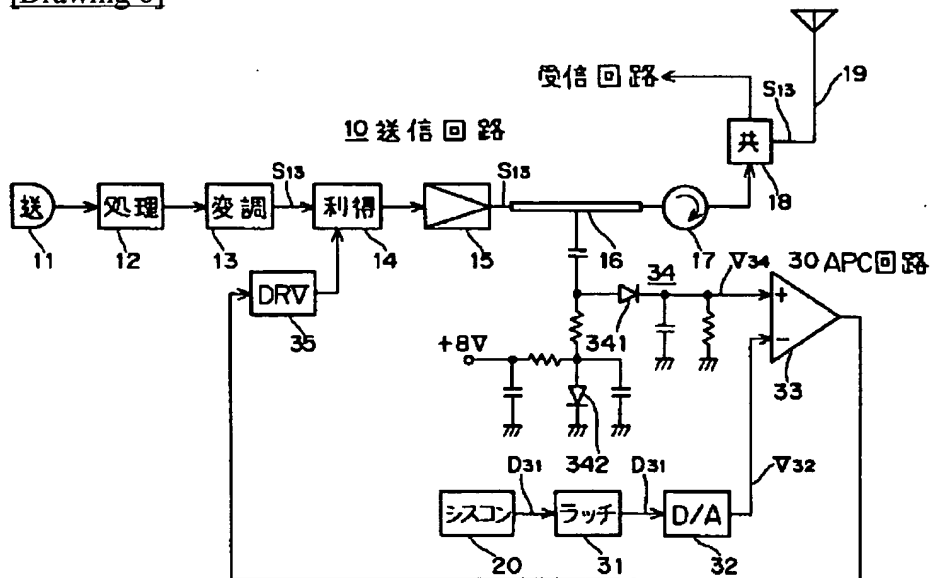
[Drawing 4]



[Drawing 5]



[Drawing 6]



[Translation done.]